



Product Specification

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(\spadesuit) Final Specification

Title	15.6" HD TFT LCD

Customer	ASUS
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LP156WH4
Suffix	TLN2

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
Please return 1 copy for your your signature and comment	

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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Mar. 05, 2012	-	First Draft (Preliminary Specification)	
0.1	May. 23, 2012	14	Update Optical Characteristics	0.1
		15	Update Gray Scale	
		19	Mechanical Characteristics Update (Drawing)	
		29~31	EDID Update	
1.0	Jun. 25, 2012	-	Final Draft	1.0

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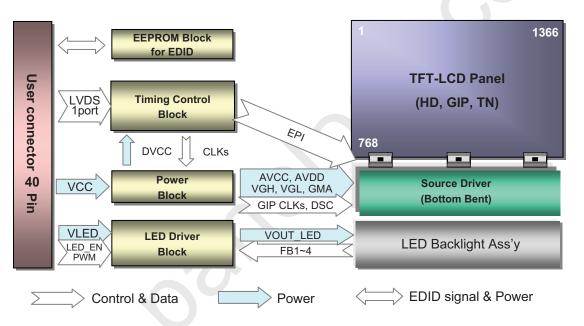




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1. General Description

The LP156WH4 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.6 inches diagonally measured active display area with HD resolution (1366 horizontal by 768 vertical pixel array). Each pixel is divided into Red, Green and Blue subpixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors. The LP156WH4 has been designed to apply the interface method that enables low power, high speed, low EMI. The LP156WH4 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the subpixels, the LP156WH4 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.6 inches diagonal
Outline Dimension	359.3(H, typ) × 209.5(V, typ) × 5.5(D,max) [mm]
Pixel Pitch	0.252 × 0.252 mm
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	220 cd/m ² (Typ.5 point @ PWM Duty = 100%)
Power Consumption	Total 4.3 W(Typ.) Logic : 0.7W (Typ.@ Mosaic), B/L : 3.6W (Typ.@ VLED 12V)
Weight	420g (Max.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Glare treatment (3H) of the front Polarizer
RoHS Comply	Yes
BFR / PVC / As Free	Yes for all

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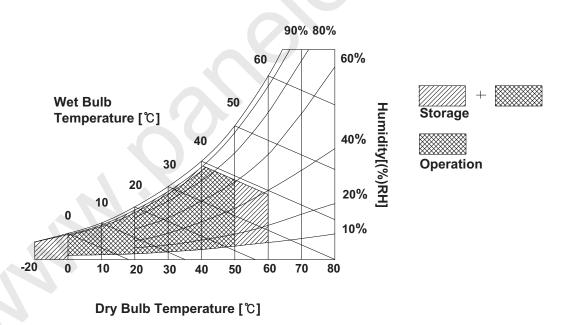
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min	Max	Offics		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39°C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

The LP156WH4 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL.with LED Driver.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Cymphol	Values				Notes	
rarameter		Symbol	Min	Тур	Max	Unit	Notes
LOGIC:							
Power Supply Input Voltage		Vcc	3.0	3.3	3.6	V	1
Power Supply Input Current	Mosaic	Icc	-	220	260	mA	2
Power Consumption		Pcc	-	0.7	0.9	W	2
Power Supply Inrush Current		Icc_p	- (-	1500	mA	3
LVDS Impedance		ZLVDS	90	100	110	Ω	4
BACKLIGHT : (with LED Drive	r)						
LED Power Input Voltage		VLED	7.0	12.0	21.0	V	5
LED Power Input Current		ILED	-	300	335	mA	6
LED Power Consumption	LED Power Consumption			3.6	4.0	W	6
LED Power Inrush Current		ILED_P	-	-	1500	mA	7
PWM Duty Ratio			5	-	100	%	8
PWM Jitter		-	0	-	0.2	%	9
PWM Impedance		Zpwm	20	40	60	kΩ	
PWM Frequency		FPWM	200	-	1000	Hz	10
PWM High Level Voltage		V _{PWM_H}	3.0	-	5.3	V	
PWM Low Level Voltage		V _{PWM_L}	0	-	0.3	V	
LED_EN Impedance		Zpwm	20	40	60	kΩ	
LED_EN High Voltage		VLED_EN_H	3.0	-	5.3	V	
LED_EN Low Voltage	VLED_EN_L	0	-	0.3	V		
Life Time		12,000	-	-	Hrs	11	

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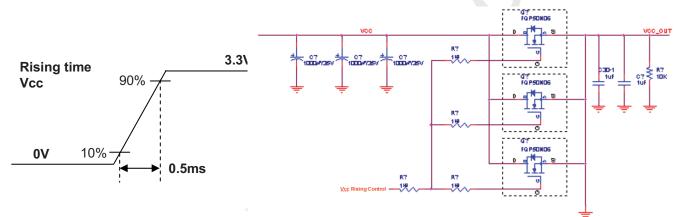




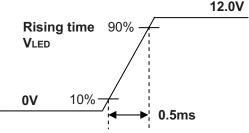
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Note)

- 1. The measuring position is the connector of LCM and the test conditions are under 25 ℃, fv = 60Hz, Black pattern.
- 2. The specified Icc current and power consumption are under the Vcc = 3.3V, $25\,^{\circ}$ C, fv = 60Hz condition and Mosaic pattern.
- 3. This Spec. is the max load condition for the cable impedance designing. The below figures are the measuring Vcc condition and the Vcc control block LGD used. The Vcc condition is same as the minimum of T1 at Power on sequence.



- 4. This impedance value is needed for proper display and measured form LVDS Tx to the mating connector.
- 5. The measuring position is the connector of LCM and the test conditions are under 25 °C.
- 6. The current and power consumption with LED Driver are under the Vled = 12.0V, $25^{\circ}C$, Dimming of Max luminance and White pattern with the normal frame frequency operated(60Hz).
- The below figures are the measuring Vled condition and the Vled control block LGD used.
 VLED control block is same with Vcc control block.



- 8. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 9. If Jitter of PWM is bigger than maximum, it may induce flickering.
- 10. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 11. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value specified in table 7. under general user condition.

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3-2. Interface Connections

This LCD employs two interface connections, a 40 pin connector used for the module electronics interface and the other connector used for the integral backlight system.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	NC	No Connection.	[Interface Chip]
2	VCC	LCD Logic and driver power (3.3V Typ.)	1. LCD :
3	VCC	LCD Logic and driver power (3.3V Typ.)	SiW, SW0645 (LCD Controller)
4	V EEDID	DDC Power (3.3V)	including LVDS Receiver
5	NC	No Connection.	2. System : THC63LVDF823A
6	Clk EEDID	DDC Clock	or equivalent
7	DATA EEDID	DDC Data	* Pin to Pin compatible with LVDS
8	ORX0-	Negative LVDS differential data input	[Connector]
9	ORX0+	Positive LVDS differential data input	LSMtron GT05Q-40S-H10-M or
10	GND	LCM Ground	HD1S040HA2, JAE
11	ORX1-	Negative LVDS differential data input	
12	ORX1+	Positive LVDS differential data input	[Mating Connector]
13	GND	LCM Ground	20453-040T-0x, I-PEX or equivalent
14	ORX2-	Negative LVDS differential data input	
15	ORX2+	Positive LVDS differential data input	[Connector pin arrangement]
16	GND	LCM Ground	40 1
17	ORXC-	Negative LVDS differential clock input	
18	ORXC+	Positive LVDS differential clock input	
19	GND	LCM Ground	
20	NC	No Connection	[LCD Module Rear View]
21	NC	No Connection	
22	GND	LCM Ground	
23	NC	No Connection	
24	NC	No Connection	
25	GND	LCM Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	LCM Ground	
29	NC	No Connection	
30	NC	No Connection	
31	GND	LCM Ground (LED Backlight Ground)	
32	GND	LCM Ground (LED Backlight Ground)	
33	GND	LCM Ground (LED Backlight Ground)	
34	NC	No Connection.	
35	PWM	System PWM Signal input for dimming	
36	LED_EN	LED Backlight On/Off	
37	NC	No Connection	
38	VLED	LED Backlight Power (7V-21V)	
39	VLED	LED Backlight Power (7V-21V)	
40	VLED	LED Backlight Power (7V-21V)	
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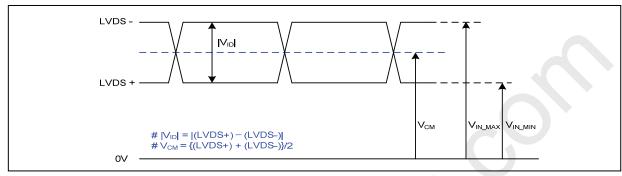




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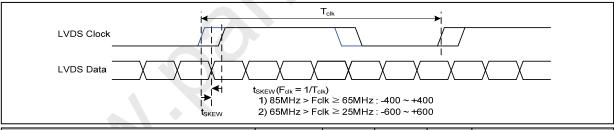
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

3-3-2. AC Specification



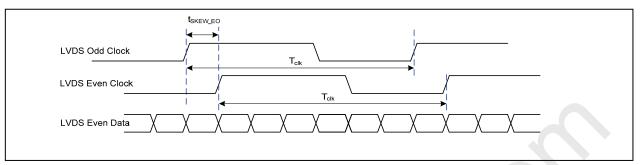
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{skew}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{skew}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{skew_eo}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-

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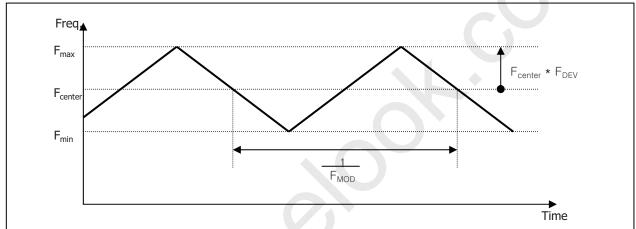




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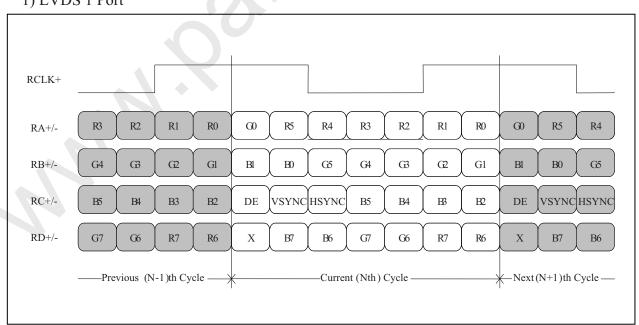
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >

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Condition: VCC =3.3V





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3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 4. TIMING TABLE

		1010 11 111					
ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	-	70.0	-	MHz	
	Period	t _{HP}	1464	1492	1528		
Hsync	Width	t _{wh}	32	48	62	tCLK	
	Width-Active	t _{wha}	1366	1366	1366		
	Period	t _{VP}	776	782	792		
Vsync	Width	t _{wv}	2	5	8	tHP	
	Width-Active	t _{wva}	768	768	768		
	Horizontal back porch	t _{HBP}	34	42	60	+CLI/	
Data	Horizontal front porch	t _{HFP}	32	36	40	tCLK	
Enable	Vertical back porch	t _{VBP}	4	6	12	HIID	
	Vertical front porch	t _{VFP}	2	3	4	tHP	



High: 0.7VCC Data Enable, Hsync, Vsync Low: 0.3VCC 0.5 Vcc **DCLK** t_{HP} Hsync twha t_{HFP} t_{HBP} Data Enable Vsync t_{VFP} twva t_{VBP} Data Enable 11/31 Ver. 1.0 Jun. 25, 2012





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3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	ΞD					GRE	EEN					BL	UE		
`	30101	MSE	3				LSB	MSI	3				LSB	MS	3				LSB
	,	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0
	Black	0	0			0	0	0	0		0	0	0	0	0	0	0	0	0
	Red	1	1	.1	. 1	1	1	0	0		0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	. 1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	. 1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN					 														
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE					· · · · · ·						· · · · · ·								
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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Global LCD Panel Exchange Center

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3-7. Power Sequence

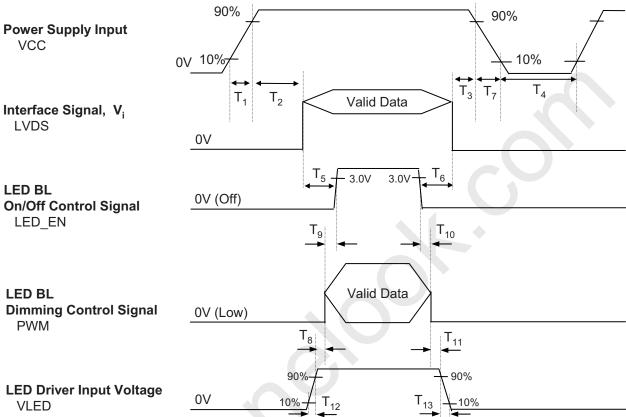


Table 6. POWER SEQUENCE TABLE

Logic		Value		Units	LED		Value		Units
Parameter	Min.	Тур.	Max.	UTIILS	Parameter	Min.	Тур.	Max.	Ullits
T ₁	0.5	-	10	ms	T ₈	10	ı	-	ms
T ₂	0	-	50	ms	T ₉	0	ı	-	ms
T ₃	0	1	50	ms	T ₁₀	0	ı	-	ms
T ₄	400	ı	ı	ms	T ₁₁	10	ı	-	ms
T ₅	200	1	1	ms	T ₁₂	0.5	1	-	ms
T ₆	200	1	-	ms	T ₁₃	0	1	5000	ms
T ₇	3	1	10	ms					

- 1. Do not insert the mating cable when system turn on.
- 2. Valid Data have to meet "3-3. LVDS Signal Timing Specifications"
- 3. LVDS, LED_EN and PWM need to be on pull-down condition on invalid status.
- 4. LGD recommend the rising sequence of VLED after the Vcc and valid status of LVDS turn on.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

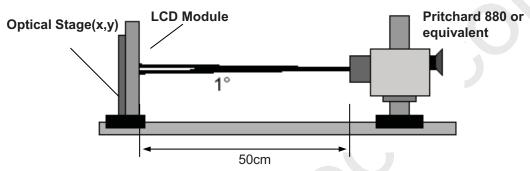


Table 9. OPTICAL CHARACTERISTICS

Table 7. OPTICAL CHARACTERISTICS

Ta=25°C, Vcc=3.3V, f_V =60Hz, f_{CLK} = 70.0MHz

Demonstra	0		Values		1.1	NI-4
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	400	-]	1
Surface Luminance, white	L _{WH}	185	220	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.4	1.6]	3
Response Time	Tr_{R} + Tr_{D}	-	16	-	ms	4
Color Coordinates		,]	
RED	RX	0.579	0.609	0.639	1	
	RY	0.318	0.348	0.378	[
GREEN	GX	0.322	0.352	0.382	[
	GY	0.576	0.606	0.636		
BLUE	BX	0.120	0.150	0.180	[
	BY	0.066	0.096	0.126		
WHITE	WX	0.283	0.313	0.343	[
	WY	0.299	0.329	0.359]	
Viewing Angle						5
x axis, right(Φ=0°)	Θr	40	45	-	degree	
x axis, left (⊕=180°)	Θl	40	45	-	degree	
y axis, up (Φ=90°)	Θu	10	15	-	degree	
y axis, down (Φ=270°)	Θd	30	35	-	degree	
Gray Scale						6

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Note)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, ... L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(\mathsf{L}_1, \mathsf{L}_2, \, \dots \, \mathsf{L}_{13})}{\text{Minimum}(\mathsf{L}_1, \mathsf{L}_2, \, \dots \, \mathsf{L}_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_V = 60Hz$$

Gray Level	Luminance [%] (Typ)
LO	0.16
L7	1.48
L15	5.57
L23	12.0
L31	20.8
L39	35.3
L47	55.5
L55	79.1
L63	100





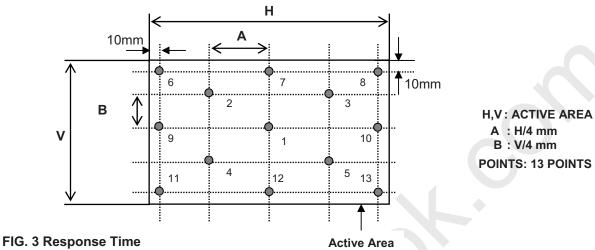
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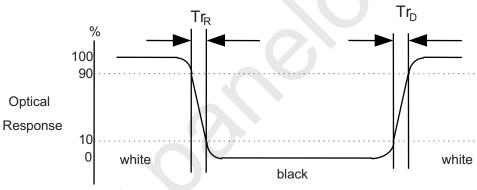
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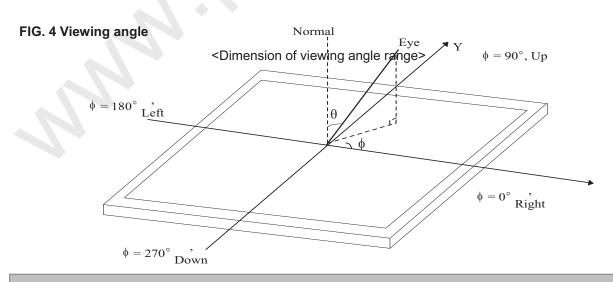
FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>



The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".









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5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP156WH4. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	359.3 ± 0.5mm		
Outline Dimension	Vertical	209.5 ± 0.5mm		
	Thickness	5.5mm (max)		
Bezel Area	Horizontal	350.0 ± 0.5mm		
bezei Area	Vertical	197.1 ± 0.5mm		
Active Diepley Area	Horizontal	344.232 mm		
Active Display Area	Vertical	193.536 mm		
Weight	420g (Max.)			
Surface Treatment	Glare treatment(3H) of the front pola	arizer		

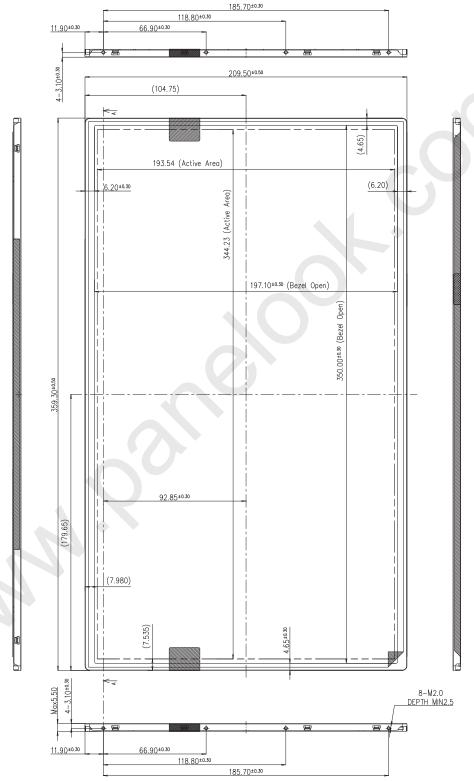




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<FRONT VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm



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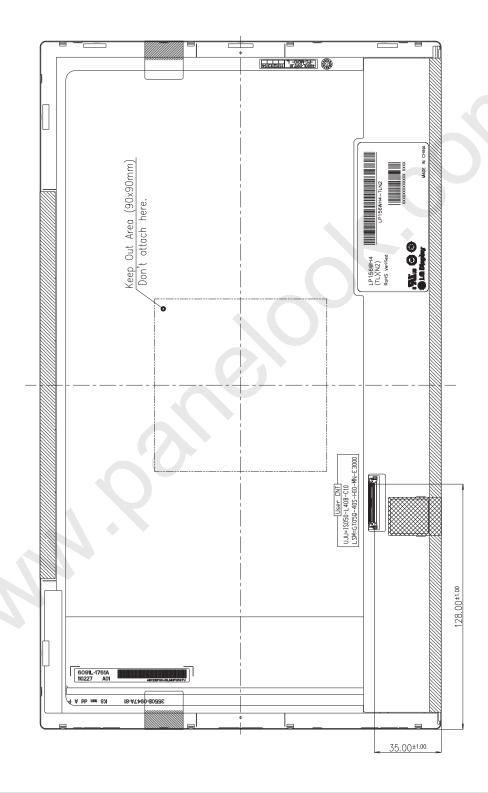




Product Specification

<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm



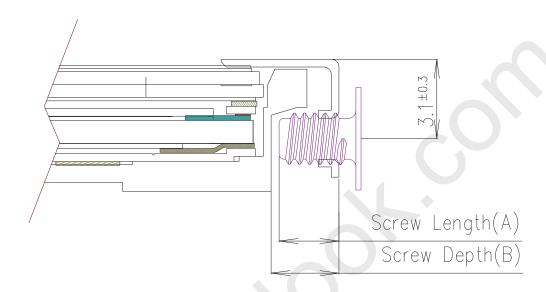
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[DETAIL $\mathsf{DESCRIPTION}$ OF SIDE MOUNTING SCREW]



- * Mounting Screw Length (A)
 - = 2.0(Min) / 2.5(Max)
- * Mounting Screw Hole Depth (B)
 - = 2.5(Min)
- * Mounting hole location : 3.1(Typ)
- * Torque : 2.0 kgf.cm(Max)

(Measurement gauge: torque meter)

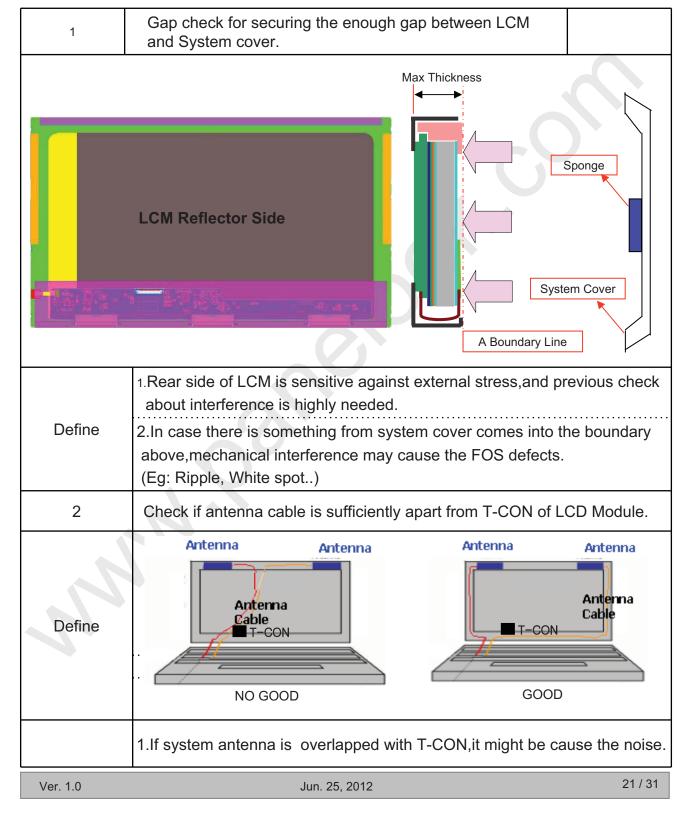
Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.





Product Specification

LGD Proposal for system cover design.(Appendix)

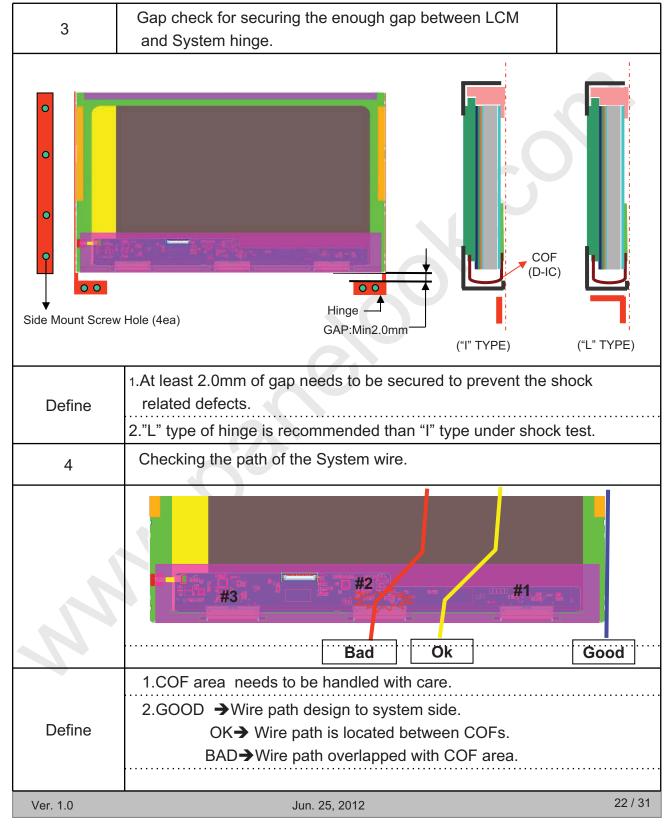






Product Specification

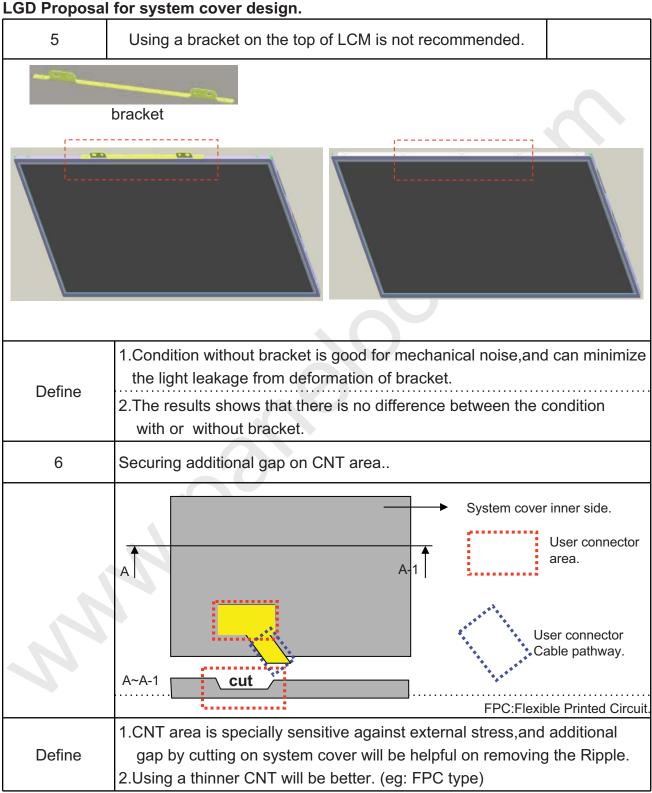
LGD Proposal for system cover design.







Product Specification



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Product Specification

6. Reliability

Environment test condition

No.	Test Item	Conditions					
1	High temperature storage test	Ta= 60°C, 240h					
2	Low temperature storage test	Ta= -20°C, 240h					
3	High temperature operation test	Ta= 50°C, 50%RH, 240h					
4	Low temperature operation test	Ta= 0°C, 240h					
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis					
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 2ms for all six faces)					
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr					

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.





Product Specification

7. International Standards

7-1. Safety

- a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1: General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association. Information Technology Equipment Safety Part 1: General Requirements.
- c) EN 60950-1:2006 + A11:2009, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1:2005, Second Edition, The International Electrotechnical Commission (IEC). Information Technology Equipment Safety Part 1 : General Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003





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Product Specification

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



A,B,C: SIZE(INCH)

E: MONTH F~ M: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

D:YEAR

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 22 pcs

b) Box Size: 478x365x288mm





Product Specification

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.





Product Specification

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.





Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	0	00	Header	0.0	0000000
	1	01	Header	FF	1111111
H e a d e r	2	02	Header	FF	1111111
n a	3	03	Header	FF	1111111
Н	5	04	Header	FF	11111111
	6	05 06	Header Header	FF FF	1111111
	7	07	Header	00	0000000
1	8	08	EISA manufacture code (3 Character ID) LGD	30	0011000
u c t n	9	09	EISA manufacture code (Compressed ASC II)	E4	1110010
d u	10	0 A	Panel Supplier Reserved - Product Code 038Eh	8E	100011
Prod ersio	11	0B	(Hex. LSB first)	03	000000
	12	0C	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	000000
4 /	13	0 D	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	000000
dor ID	14	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	000000
	15	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	000000
en ED	16	10	Week of Manufacture 00 weeks	0.0	000000
	17	11	Year of Manufacture 2012 years	16	000101
	18	12	EDID structure version # = 1	01	000000
	19	13	EDID revision # = 3	03	000000
12	20	14	Video input Definition = Digital signal	80	100000
ısplay "am ete	21	15	Max H image size (Rounded cm) = 34 cm	22	001000
) [0					
D ispi aram	22	16	Max V image size (Rounded cm) = 19 cm	13	000100
ar	23	17	Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	011110
Ь	24	18	Feature Support (no_DPMS, no_Active Off/Very Low Power, RGB color display, Timing BLK 1,no GTF)	0 A	000010
d i	25	19	Red/Green Low Bits (RxRy/GxGy)	01	000000
anel Color Coordi	26	1A	Blue/White Low Bits (BxBy/WxWy)	A 5	101001
00	27	1B	Red X Rx = 0.609	9C	100111
C	28	1C	Red Y Ry = 0.348	59	010110
) r	29	1D	Green X $Gx = 0.352$	5A	010110
) le	30	1E	Green Y Gy = 0.606	9B	100110
C^{ϵ}	31	1F	Blue X Bx = 0.150	26	001001
1	32	20		18	000110
n e					
	33	21	White X $Wx = 0.313$	50	010100
s P	34	22	White Y Wy = 0.329	54	010101
ing	35	23	Established timing 1 (00h if not used)	00	000000
im	36	24	Established timing 2 (00h if not used)	00	000000
E S	37	25	Manufacturer's timings (00h if not used)	00	000000
	38	26	Standard timing ID1 (01h if not used)	01	000000
ID	39	27	Standard timing ID1 (01h if not used)	01	000000
	40	28	Standard timing ID2 (01h if not used)	01	000000
in	41	29	Standard timing ID2 (01h if not used) Standard timing ID2 (01h if not used)	01	000000
Standard Timing		2A	Standard timing ID3 (01h if not used)	01	
Ţ.	43	2B 2C	Standard timing ID3 (01h if not used) Standard timing ID4 (01h if not used)	01	000000
1 1	44	2C 2D		01	000000
ra	46	2 E	Standard timing ID4 (01h if not used) Standard timing ID5 (01h if not used)	01	000000
1 a	47	2 E 2 F	Standard timing ID5 (01h ii not used) Standard timing ID5 (01h if not used)	01	000000
nc	48	30	Standard timing ID6 (01h ir not used)	01	000000
ta	49	31	Standard timing ID6 (01h ir not used) Standard timing ID6 (01h ir not used)	01	000000
S	50	32	Standard timing ID7 (01h if not used)	01	000000
	51	33	Standard timing ID7 (01h if not used)	01	000000
	52	34	Standard timing ID8 (01h if not used)	01	000000
	53	35	Standard timing ID8 (01h if not used)	01	000000

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Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments		Value (Hex)	Value (Bin)
1 #	54	36	Pixel Clock/10,000 (LSB)	70 MHz @ 60Hz	58	01011000
	55	37	Pixel Clock/10,000 (MSB)		1 B	00011011
	56	38	Horizontal Active (lower 8 bits)	1366 Pixels	56	01010110
	57	39	Horizontal Blanking(Thp-HA) (lower 8 bits)	126 Pixels	7 E	01111110
÷	58	3 A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)		50	01010000
to	59	3 B	Vertical Avtive	768 Lines	0.0	00000000
i.	60	3 C	Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels)	14 Lines	0 E	00001110
c r	61	3 D	Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits)		30	00110000
e s	62	3 E	Horizontal Sync. Offset (Thfp)	36 Pixels	24	00100100
D	63	3 F	Horizontal Sync Pulse Width (HSPW)	48 Pixels	30	00110000
s_{ι}	64	40	Vertical Sync Offset(Tvfp) : Sync Width (VSPW)	3 Lines : 5 Lines	35	00110101
Timing Descriptor#	65	41	Horizontal Vertical Sync Offset/Width (upper 2bits)		0.0	00000000
in	66	42	Horizontal Image Size (mm)	344 mm	58	01011000
T	67	43	Vertical Image Size (mm)	194 mm	C 2	11000010
	68	44	Horizontal Image Size / Vertical Image Size		10	00010000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)		0.0	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)		00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, note: LSB is set to '1' if panel is DE-timing only. H/V can be ignored.	Hsync_NEG), DE only	19	00011001
	72	48	Flag		00	00000000
	73	49	Flag		00	00000000
0.	74	4 A	Flag		00	00000000
#	75	4 B	Data Type Tag (Descriptor Defined by manufacturer)		00	00000000
, ·	76	4 C	Flag		00	00000000
) te	77	4 D	Descriptor Defined by manufacturer		00	00000000
rip	78	4 E	Descriptor Defined by manufacturer		00	00000000
sc	79	4 F	Descriptor Defined by manufacturer		00	00000000
Timing Descriptor #2	80	50	Descriptor Defined by manufacturer		00	00000000
, r	81	51	Descriptor Defined by manufacturer		00	00000000
n g	82	52	Descriptor Defined by manufacturer		00	00000000
n i	83	53	Descriptor Defined by manufacturer		00	00000000
r i i	84	54	Descriptor Defined by manufacturer		00	00000000
	85	55	Descriptor Defined by manufacturer		00	00000000
	86	56	Descriptor Defined by manufacturer		00	00000000
	87	57	Descriptor Defined by manufacturer		00	00000000
	88	58	Descriptor Defined by manufacturer		00	00000000
	89	59	Descriptor Defined by manufacturer		0.0	00000000
#3	90	5 A	Flag		00	00000000
	91	5 B	Flag		0.0	00000000
	92	5C	Flag		0.0	00000000
	93	5 D	Data Type Tag (ASCII String)		FE	11111110
) r	94	5 E	Flag		0.0	00000000
) 1(95	5 F	ASCII String	L	4C	01001100
Timing Descripto	96	60	ASCII String	G	47	01000111
	97	61	ASCII String		20	00100000
	98	62	ASCII String	D	44	01000100
7	99	63	ASCII String	i	69	01101001
n S	100	64	ASCII String	S	73	01110011
n i	101	65	ASCII String	p	70	01110000
ri.	102	66	ASCII String	1	6C	01101100
1	103	67	ASCII String	a	61	01100001
	104	68	ASCII String	у	79	01111001
	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II		0 A	00001010
	106	6 A	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II		20	00100000
			7		- v	

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Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
Timing Descriptor #4	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (ASCII String)	FE	11111110
	112	70	Flag	00	00000000
	113	71	ASCII String L	4C	01001100
	114	72	ASCII String P	50	01010000
	115	73	ASCII String 1	31	00110001
	116	74	ASCII String 5	35	00110101
	117	75	ASCII String 6	36	00110110
	118	76	ASCII String W	57	01010111
	119	77	ASCII String H	48	01001000
	120	78	ASCII String 4	34	00110100
	121	79	ASCII String -	2D	00101101
	122	7A	ASCII String T	54	01010100
	123	7B	ASCII String L	4C	01001100
	124	7C	ASCII String N	4E	01001110
	125	7D	ASCII String 2	32	00110010
Checksu	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	C 6	11000110

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